6.26

1. S = 256, t = 22, s = 8, b = 2

2. S = 4, t = 28, s = 2, b = 2

3. S = 1024, t = 19, s = 10, b = 3

4. S = 8, t = 26, s = 3, b = 3

5. S = 1024, t = 10, s = 17, b = 5

6. S = 1024, t = 8, s = 19, b = 5

6.27

Cache 1: C = 2048, S = 256

Cache 2: B = 4, E = 4

Cache 3: t = 25, s = 6

Cache 4: B = 32, b = 5

6.29

A: No memory address will hit set 2

B: 0x18F0, 0x18F1, 0x18F2, 0x18F3, 0x00B0, 0x00B1, 0x00B2, 0x00B3

C: 0x0E34, 0x0E35, 0x0E36, 0x0E37

D: 0x1BDC, 0x1BDD, 0x1BDE, 0x1BDF

6.30

A: 4 - 11 are the cache tag, 2 and 3 are the cache set index, 0 and 1 are the cache block offset

B: First row-No hit, Read Value Unknown; second row- hit, Read value unknown; third row- hit, 0xC0